

Notice of References Cited	Application/Control No. 10/727,190	Applicant(s)/Patent Under Reexamination PARK, HEONCHUL	
	Examiner Benjamin P. Geib	Art Unit 2181	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,842,848	01-2005	Hokenek et al.	712/215
*	B	US-2003/0046517	03-2003	Lauterbach, Gary R.	712/214
*	C	US-6,938,147	08-2005	Joy et al.	712/28
*	D	US-6,295,600	09-2001	Parady, Bodo	712/228
*	E	US-2004/0034759	02-2004	Katzman et al.	712/1
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Interleaving: A Multithreading Technique Targeting Multiprocessors and Workstations"; James Laudon, Anoop Gupta, Mark Horowitz; 1994
	V	"A Fine-Grain Multithreading Superscalar Architecture"; Mat Loikkanen, Nader Bagherzadeh; 1996; IEEE
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.